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Microcontroller MCQ

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Q1. In 8086 microprocessor , the address bus is _____ bit wide

- **A.** 10 bit
- **B. 20 bit**
- **C.** 16 bit
- **D.** 12 bit

Q2. What is the file extension is used to load in a microcontroller to execute an instruction?

- **A.** .doc
- **B.** .c
- **C.** .txt
- **D. .hex**

Q3. What are the two instruction set architecture (ISA) classifications?

- **A.** SIDC & BIDC
- **B. CISC & RISC**
- **C.** CISC & VISC

Q4. What is internal RAM memory of 8051 microprocessor?

- **A.** 32 bytes
- **B.** 64 bytes

- **C. 128 bytes**
- **D.** 256 bytes

Q5. How many timers/counter 8051 16-bit microprocessor have?

- **A.** 1
- **B. 2**
- **C.** 5
- **D.** 8

Q6. Register that is used to holds the memory address of the next instruction to be executed is

- **A.** Program Memory
- **B. Program Counter**
- **C.** Control Unit
- **D.** Instruction decoder

Q7. The 8051 microprocessor has _____ parallel I/O ports.

- **A.** 2
- **B.** 3
- **C. 4**
- **D.** 5

Q8. Which instructions affect the program counter?

- **A.** Call & Return
- **B. Call & Jump**
- **C.** Push & Pop
- **D.** Return & Jump

Q9. Which register is used to store the output generated by ALU?

- **A.** Special Function Register
- **B.** Timer Register
- **C. Accumulator**
- **D.** Stack Pointer

Q10. .... signal control the flow of data

- **A.** DTR
- **B.** RTS
- **C. Both 1 & 2**
- **D.** None of the above

Q11. Which of the following is the function of the TMOD register?

- **A.** It is used to interrupt the timer
- **B.** It is used to load the count of the timer
- **C. It is used to set different timer's or counter's to their appropriate modes**
- **D.** It Is the destination or the final register where the result is obtained after the operation of the timer

Q12. Which of the following is the function of the SCON register?

- **A.** control SMOD registers
- **B.** control SBUF and SMOD registers
- **C. program the start bit, stop bit, and data bits of framing**
- **D.** None of the above

Q13. Which of the following is the bit of the IE register is used to enable TxD/RxD interrupt?

- **A.** IE.D2
- **B.** IE.D3
- **C. IE.D4**
- **D.** IE.D5

Q14. Which of the following pin of the LCD is used for adjusting its contrast?

- **A. pin no 3**
- **B.** pin no 1
- **C.** pin no 4
- **D.** pin no 2

Q15. Point out the correct statement about about DAC0808?

- **A.** It has current as an output
- **B. Parallel digital data to analog data conversion**
- **C.** Both A & B
- **D.** None of the above

Q16. Which of the following register usually store the output generated by ALU in several arithmetic and logical operations?

- **A.** Special Function Register
- **B.** Timer Register
- **C. Accumulator**
- **D.** Stack Pointer

Q17. Which of the following operations are performed by stack pointer during its incremental phase?

- **A.** Pop
- **B. Push**
- **C.** Return
- **D.** None of the above

Q18. Which of the following is the default value of stack once after the system undergoes the reset condition?

- **A.** 08H
- **B.** 09H
- **C.** 00H
- **D. 07H**

Q19. Whichn of the following commands are used for addressing the off-chip data and associated codes respectively by data pointer?

- **A.** MOVC & MOVY
- **B.** MOVY & MOVB
- **C.** MOVZ & MOVA
- **D. MOVX & MOVC**

Q20. Which of the following is the bit addressing range of addressable individual bits over

the on-chip RAM?

- **A. 00H to 7FH**
- **B.** 01H to 7FH
- **C.** 00H to FFH
- **D.** 80H to FFH

Q21. The assembler list file generated by an assembler mainly includes

- **A.** Binary codes
- **B.** Offset for each instruction
- **C.** Assembly language statements
- **D. All of the Above**

Q22. Which operations are performed by stack pointer during its incremental phase?

- **A.** Pop
- **B. Push**
- **C.** Return
- **D.** None of above

Q23. Which register usually store the output generated by ALU in several arithmetic and logical operations?

- **A.** Stack Pointer
- **B.** Timer Register
- **C. Accumulator**
- **D.** Special Function Register

Q24. What kind of instructions usually affect the program counter?

- **A.** Push & Pop
- **B. Call & Jump**
- **C.** Call & Return
- **D.** Return & Jump

Q25. .... is the required baud rate for an efficient operation of serial port devices in 8051 microcontroller?

- **A.** 1200
- **B.** 4800
- **C. 9600**
- **D.** 2400

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