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Computer Architecture MCQ Quiz

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Q1. Which of the following format is usually used to store data?

- **A.** Decimal
- **B. BCD**
- **C.** Octal
- **D.** Hexadecimal

Q2. Which of the following memory device is generally made of semiconductors?

- **A. RAM**
- **B.** Floppy disk
- **C.** Hard-disk
- **D.** Cd disk

Q3. Which of the following is the bus that is used to connect the monitor to the CPU?

- **A.** PCI bus
- **B.** Ram bus
- **C.** Memory bus
- **D. SCSI bus**

Q4. what does ANSI stands for?

- **A.** American National Standard Interface
- **B.** American Network Standard Interfacing

- **C. American National Standards Institute**
- **D.** American Network Security Interrupt

Q5. Which type of memory assignment is used in Intel processors?

- **A.** Big Endian
- **B. Little Endian**
- **C.** Medium Endian
- **D.** None of the above

Q6. .... used to effectively utilize main memory?

- **A.** Dynamic linking
- **B.** Address binding
- **C. Dynamic loading**
- **D.** None of the above

Q7. Which of the following can be considered as most advanced ROM ?

- **A.** RAM
- **B.** PROM
- **C.** DRAM
- **D. EEPROM**

Q8. Which of the following is a Volatile Memory?

- **A.** ROM
- **B. RAM**
- **C.** DROM
- **D.** EPROM

Q9. Which of the following is an important data transfer technique ?

- **A.** CAM
- **B.** CAD
- **C. DMA**
- **D.** MMA

Q10. A keyboard has .... type of asynchronous transfer mode ?

- **A.** Parralel
- **B.** Optimal
- **C. Serial**
- **D.** Joint

Q11. The term "VAD" means?

- **A.** Viscous Address
- **B.** Velocity Address
- **C. Vector Address**
- **D.** Volatile Address

Q12. Which of the following system was used extensively by early mini computers?

- **A.** Decimal number
- **B.** Binary number
- **C. Octal number**
- **D.** Hexadecimal number

Q13. Which of the following memory is assembled between main memory and CPU ?

- **A.** Registers
- **B.** Secondary Memory
- **C.** Primary Memory
- **D. Cache Memory**

Q14. Does CPU provide enabling signals control bus?

- **A. Yes**
- **B.** No

Q15. Which of the following language developed for business applications?

- **A.** C
- **B.** C++

- **C. Cobol**
- **D.** Pascal

Q16. Microprocessor 8085 is the enhanced version of ....

- **A. 8080**
- **B.** 8088
- **C.** 6800
- **D.** All of the above

Q17. We reduce the memory access time with the help of ....

- **A.** Heaps
- **B. Cache**
- **C.** SDRAM
- **D.** None of the above

Q18. Which of the following language is used in a source program?

- **A.** Assembly language
- **B. High-level language**
- **C.** Low-level language
- **D.** Machine level language

Q19. Which of the following is the bus used to connect the monitor to the CPU?

- **A.** PCI bus
- **B.** Rambus
- **C. SCSI bus**
- **D.** Memory bus

Q20. The ISA standard Buses are used to connect ....

- **A.** GPU and processor
- **B.** RAM and processor
- **C. Harddisk and Processor**
- **D.** None of the above

Q21. The control unit controls other units by generating ....

- **A. Timing signals**
- **B.** Control signals
- **C.** Transfer signals
- **D.** Command Signals

Q22. The control unit controls other units by generating ....

- **A. Timing signals**
- **B.** Control signals
- **C.** Transfer signals
- **D.** Command Signals

Q23. Which bus structure is usually used to connect I/O devices?

- **A.** Rambus
- **B. Star bus**
- **C.** Single bus
- **D.** Multiple bus

Q24. The usual BUS structure used to connect the I/O devices is ....

- **A.** Star BUS structure
- **B. Single BUS structure**
- **C.** Multiple BUS structure
- **D.** Node to Node BUS structure

Q25. The circuit used for the request line is a ....

- **A. Open-drain**
- **B.** EX-OR circuit
- **C.** Nand circuit
- **D.** Open-collector

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